=> d his (FILE 'USPAT' ENTERED AT 17:52:11 ON 03 APR 1997) L1 121 S (STATUS (2A) RELAY) (P) (POWER)

=> d 1-10

- 1. 5,606,307, Feb. 25, 1997, Control circuit for an automobile; Jack Kuan, 340/426; 180/287; 307/10.3; 340/428, 430; 379/44 [IMAGE AVAILABLE]
- 2. 5,555,438, Sep. 10, 1996, Method for synchronously transferring serial data to and from an input/output (I/O) module with true and complement error detection coding; Steven P. Blech, et al., 395/850; 364/221.9, 238.7, 271.1, 271.2, 271.4, DIG.1; 371/70; 395/200.17, 881 [IMAGE AVAILABLE]
- 3. 5,537,026, Jul. 16, 1996, Method and apparatus for power controller operation using master and slave firing units; Wayne S. Estes, et al., 323/239; 361/57 [IMAGE AVAILABLE]
- 4. 5,532,894, Jul. 2, 1996, Overvoltage protection circuit; Michael C. Sweaton, 361/18; 307/43; 323/251; 363/65 [IMAGE AVAILABLE]
- 5. 5,519,223, May 21, 1996, Apparatus and method for automated collimator exchange; Paul Hug, et al., 250/363.1; 378/148 [IMAGE AVAILABLE]
- 6. 5,506,938, Apr. 9, 1996, Expert system development support system and expert system environment utilizing a frame processing technique; Chihiro Fukui, et al., 395/62 [IMAGE AVAILABLE]
- 7. 5,506,790, Apr. 9, 1996, Single-chip microcomputer programmable power distributor; Sanh K. Nguyen, 364/492; 307/38, 115; 364/141, 483 [IMAGE AVAILABLE]
- 8. 5,506,776, Apr. 9, 1996, Steering control system for vehicle; Takehiko Fushimi, et al., 364/424.051; 180/413 [IMAGE AVAILABLE]
- 9. 5,500,806, Mar. 19, 1996, Data logging in a voltage regulator controller; Michael A. Bellin, et al., 364/492; 323/255; 324/416; 364/551.01, 554 [IMAGE AVAILABLE]
- 10. 5,479,618, Dec. 26, 1995, I/O module with reduced isolation circuitry; Kerry Van de Steeg, et al., 364/167.01, 191; 395/500, 800, 828 [IMAGE AVAILABLE]